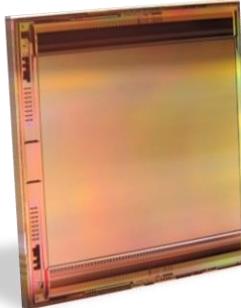


MT12815DA2

1280x1024-15 μ m DI ROIC



Features

- New DI ROIC for 1280x1024-15 μ m FPAs
- 2, 4, or 8 analog outputs with reference
- Programmable Biasing, Gain, and Windowing
- Snapshot Operation: ITR and IWR
- Low Power and High Frame Rates
- Cryogenic operation down to 65K

Technical Specifications

Array Format and Pitch	1280x1024			15 μ m \times 15 μ m			
Pixel Polarity and Type	p-on-n			QWIP, MCT, InSb, T2SL			
Input Circuit Type	Direct Injection (DI)						
Full Well Capacity	Programmable (High, Mid, and Low-Gain) Settings						
	HG: \geq 2Me-	MG: \geq 5Me-	LG: \geq 10Me-				
Detector Biasing	Programmable in steps of < 1mV (External Higher Bias Range available)						
Integration Time	Programmable from 1 μ s to 1s in 100ns steps						
Number of Analog Outputs	Programmable 2, 4, or 8 outputs with reference						
Output Swing	\leq 2.0 V with adjustable gain and offset						
Readout Gain	Programmable						
Readout Modes	Snapshot Operation: ITR and IWR Modes						
Windowing	Programmable size and location						
Frame Rate	\leq 50 Hz at full frame, upto 1000 Hz with windowing						
System Clock	Nominal 10 MHz (upto 12MHz)						
Supply Voltage	3.3 V and 1.8 V						
Power Dissipation	\leq 150 mW (50 Hz, 8 outputs)						
Input Referred Noise	\leq 850 e- rms at 77K for 10 Me- FWC						
Operating Temperature	Cryogenic ($T \geq 65K$)						
Temperature Sensor	On-chip active sensor with 1mV/K sensitivity						
Die Size / Wafer Size / DPW	Wafer: 200mm	Die Size: 21.8mm \times 21.1mm	Total Dice: 44	32 Functional Dice (Typical)			